

REMARKS

Claims 1-12 and 14-34 remain in the present application. Claim 13 has been cancelled. Applicants respectfully request further examination and reconsideration of the rejections based on the arguments set forth below.

35 U.S.C. Section 103 rejections

Paragraph 2 of the above referenced Office Action states that independent Claims 1 and 10 are rejected as being rendered obvious by Sanchez-Frank et al. (U.S. Patent No. 5,394,522), in view of Ohara et al. (U.S. Patent No. 6,366,300). Applicants respectfully traverse and assert that each of the independent claims have been amended to particularly recite configuring input output/connections for a PSOC (programmable system-on-a-chip) device.

As currently amended, independent Claim 1 recites a method for configuring input/output connections in a PSOC device comprising, displaying a graphical user interface for configuring the programmable logical device, selecting a configuration presentation from the graphical user interface, selecting an input/output connection from the PSOC device for configuration, and selecting options for configuring the input/output connections from a selection set presented in said graphical user interface.

The input/output connections recited in Claim 1 refer to the input/output pins of the PSOC device. The configuration that takes place is configuration for the pins of the PSOC device. Independent Claims 1, 10, and 18 have each been amended to explicitly point out that the input/output connections which are being configured comprise pins of a PSOC device.

Sanchez is relied upon to show configuring connections in a programmable device. Applicants understand Sanchez to teach "connections" between servers and workstations in a data processing network. For example, Sanchez recites a method of operation by which a network administrator can graphically depict a network of workstation nodes and generate configuration parameters for the various workstations (e.g., Sanchez column 2, lines 14-22). The connections of Sanchez refer to the network connections across a network (e.g., Token Ring local area network) between the workstation nodes. This is completely different from the input/output pins of a programmable device. Furthermore, applicants respectfully assert this is even more removed from the configuring of input/output pins of a PSOC device as in the claimed invention.

The addition of Ohara does not cure the defects of Sanchez. Ohara is relied upon to show an input output pin as an output device. Applicants assert that the combination of Sanchez and Ohara does not show the configuration of input output pins of a PSOC (programmable system-on-a-chip) device as in the claimed invention.

Additionally, Applicants specifically traverse any assertion that a programmable microcontroller embodiment of a PSOC device as recited in Claim 3 and Claim 17 is in any way inherent. As described above, neither Sanchez nor Ohara disclose or suggest a PSOC device. Furthermore, Sanchez, by teaching network connections of a local area network, teaches directly away from the distinguishing features of a self contained PSOC device.

Paragraph 7 of the above referenced Office Action states that independent Claim 18 is rejected as allegedly being rendered unpatentable by Sanchez and Ohara, in combination with Bergeron (US Patent No. 6,246,410) and Livingston (US Patent No. 6,750,889). Applicants respectfully traverse.

Paragraph 7 of the above referenced Office Action relies on Sanchez to show input/output connection(s) as recited in the claimed invention. As described above, Applicants point out that Sanchez teaches "connections" between servers and workstations in a data processing network. Independent Claim 18 specifically recites the input/output connections comprising one or more pins for a PSOC device. The connections of Sanchez refer to the network connections across a network (e.g., Token Ring local area network) between the workstation nodes, and thus are completely different from the input/output pins of a PSOC device as recited in the claimed invention. These deficiencies are not cured by the addition of Ohara, Bergeron, and Livingston.

Accordingly, for the rationale described above, the present invention as recited in independent Claim 18 is not rendered unpatentable by the cited combination within the meaning of 35 U.S.C. Section 103.

Paragraph 8 of the above referenced Office Action states that independent Claim 29 is rejected as allegedly being rendered unpatentable by Sanchez, in combination with Livingston and Ohara. Applicants respectfully traverse.

Paragraph 8 of the above referenced Office Action relies on Sanchez to show input/output connection(s) as recited in the claimed invention. As described above, Applicants point out that Sanchez teaches "connections" between servers and workstations in a data processing network. Independent Claim 29 specifically recites the input/output connections comprises a pin for a PSOC integrated circuit. The connections of Sanchez refer to the network connections across a network (e.g., Token Ring local area network) between the workstation nodes, and thus is completely different from the input/output pins of a PSOC integrated circuit of the claimed invention. These deficiencies are not cured by the addition of Ohara and Livingston.

Accordingly, for the rationale described above, the present invention as recited in independent Claim 29 is not rendered unpatentable by the cited combination within the meaning of 35 U.S.C. Section 103.

CONCLUSION

Applicants respectfully assert that all remaining claims (Claims 1-12 and 14-34) are now in condition for allowance and Applicants earnestly solicit such action from the Examiner.

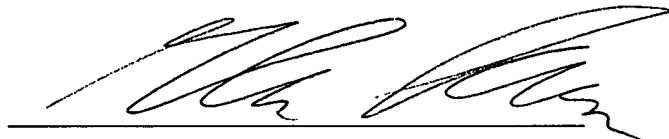
The Examiner is urged to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Please charge any additional fees or apply any credits to our PTO deposit account number: 23-0085.

Respectfully submitted,

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